Device Parameter Changes Caused by Manufacturing Fluctuations of Deep Submicron MOSFET's

Renate Sitte, Student Member, IEEE, Sima Dimitrijev, Member, IEEE, and H. Barry Harrison

Abstract—The effects of typical manufacturing fluctuations upon four electrical device parameters: threshold voltage, transconductance, substrate current and off current have been studied for deep submicron MOSFET’s (0.1 μm). The analysis reveals that the electrical parameter sensitivity in deep submicron devices differs from micron size devices, making a revision of the validity of conventional semiconductor manufacturing heuristics for future technology mandatory.

I. INTRODUCTION

While the effects of process parameter fluctuations on deep submicron devices are in the main understood for devices currently produced, little is known about deep submicron devices because little manufacturing experience has yet been accumulated. With down scaling, some physical effects may become more pronounced and dominant over others, changing the effect of manufacturing fluctuations on the device. Several 0.1 μm MOSFET’s have been designed and manufactured [1]-[4], providing reason to suspect that commercial production at this level may come to fruition in the future. While the published results claim improved device electrical characteristics, these device dimensions raise the important practical question as to whether it will be possible to reproduce these devices in larger numbers. Fluctuations which are found in current manufacturing, and which are sufficiently controlled to be considered as acceptable, may no longer be acceptable for submicron technology. It is the purpose of this paper to identify through the results of analysis, where in future submicron technology the endeavor to minimize device parameter fluctuations are most effective.

To gain insight into the effects of process parameter fluctuations on deep submicron devices a comparative study based on simulation is presented in this paper. The study consisted mainly of three parts: 1) Determination of device parameter fluctuations, based on typical, random fluctuations of processing parameters, using Monte Carlo methods; the device parameters under scrutiny were threshold voltage, transconductance, substrate current, and off-current (the drain current for Vgs = 0 V). 2) Finding for each processing parameter its effective contribution to the device fluctuation. 3) Verification (consolidation) of results.

TABLE I

<table>
<thead>
<tr>
<th>Process Parameter</th>
<th>unit</th>
<th>mean value</th>
<th>std.dev</th>
<th>mean value</th>
<th>std.dev</th>
</tr>
</thead>
<tbody>
<tr>
<td>Channel Length</td>
<td>μm</td>
<td>0.1</td>
<td>0.06</td>
<td>1.5</td>
<td>0.15</td>
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<tr>
<td>Gate Oxide Thickness</td>
<td>nm</td>
<td>4.5</td>
<td>1.5</td>
<td>30</td>
<td>3.0</td>
</tr>
<tr>
<td>Gate Oxide Charge</td>
<td>10¹⁰/cm²</td>
<td>5.0</td>
<td>3.0</td>
<td>5.0</td>
<td>3.0</td>
</tr>
<tr>
<td>Substrate Conc.</td>
<td>10¹⁵/cm³</td>
<td>7.0</td>
<td>0.7</td>
<td>1.0</td>
<td>0.1</td>
</tr>
</tbody>
</table>

Threshold Voltage Adjustment Implant

<table>
<thead>
<tr>
<th>Species</th>
<th>Boron</th>
<th>Boron</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dose</td>
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<td>3.0</td>
</tr>
<tr>
<td>Energy</td>
<td>keV</td>
<td>15</td>
</tr>
<tr>
<td>Annealing Temp.</td>
<td>°C</td>
<td>875</td>
</tr>
<tr>
<td>Annealing Time</td>
<td>s</td>
<td>2400</td>
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</table>

Source and Drain Extension Implant

<table>
<thead>
<tr>
<th>Species</th>
<th>Antimony</th>
<th>Phosphorous</th>
</tr>
</thead>
<tbody>
<tr>
<td>Dose</td>
<td>10¹¹/cm²</td>
<td>4.0</td>
</tr>
<tr>
<td>Energy</td>
<td>keV</td>
<td>15</td>
</tr>
<tr>
<td>Annealing Temp.</td>
<td>°C</td>
<td>875</td>
</tr>
<tr>
<td>Annealing Time</td>
<td>s</td>
<td>1800</td>
</tr>
</tbody>
</table>

Fig. 1. Output characteristics of 0.1 μm N-channel MOSFET. Symbols: measured data [6], lines: MINIMOS simulation.

II. CONSTITUENTS OF THE SIMULATION EXPERIMENT

Few deep submicron MOSFET’s or circuits have so far been realized [1]-[4]. We have chosen the data published by G. Sai-Halasz et al. [1] for our consideration, namely a 0.1 μm N-channel MOSFET. To be able to make comparisons and detect differences between fluctuations of deep submicron and micron size devices, all simulations were repeated with a typical 1.5 μm N-channel device. The fabrication data of these two devices are listed in Table I.

To perform the simulations MINIMOS [5] has been used as the simulation tool. Voltages and currents were obtained with precisions ±0.001 V, and ±10⁻⁹ A, respectively. While MINIMOS is well known to be accurate for micron size
To verify the simulator's accuracy for small devices, its accuracy for small devices was verified previously [6]. To assert that the simulator is sufficiently accurate and consequently an adequate simulator for such a short channel device, the simulation of I–V characteristics have been compared to the measured I–V characteristics. The comparative results are shown in Fig. 1. As can be seen the agreement between the simulation and the actual data suffices to be safely assumed valid for the analyses based on MINIMOS simulations.

III. GENERAL STUDY OF FLUCTUATION EFFECTS

To determine the device parameter fluctuations, based on typical, random fluctuations of processing parameters, each device parameter was simulated 1000 times, with data sets generated using Monte Carlo methods. To generate the data sets, each processing parameter was randomly generated from a normal distribution around its recipe value (see Table I), and a standard deviation that is found typically in current manufacturing. The results of these simulations were normalized with respect to their corresponding recipe value, and then the statistical parameters (mean values and standard deviations) were determined for each device parameter. These statistics represent fluctuation phenomena as they may be found in manufacturing, each sample point corresponding to a wafer batch average.

To illustrate any differences, the simulation results are depicted as histograms in Fig. 2. We have found that device parameter fluctuations of the deep submicron device are larger than for the micron size counterparts, although the individual processing parameter fluctuations of the smaller device are equal or proportionally less than those of the large devices. The ratio between their respective standard deviations is at least 2:1 (normalized data).

IV. ANALYTICAL STUDY OF FLUCTUATION EFFECTS

An analytical study has been performed, to find the most influencing processing parameters, which cause the increased spread of device parameters fluctuations in the small device. A mathematical model which expresses a device parameter variance in terms of the process parameter variances has been used as the starting point in the study. This model was previously applied by Graaff and Klaassen [7] in their study on dependence of circuit parameter variances on device parameter variances. The model applied to the dependence of threshold
Fig. 3. Histograms showing the device parameter sensitivity rate to processing fluctuations for (a) threshold voltage, (b) transconductance, (c) substrate current, and (d) off current. All sensitivity data were normalized with respect to the recipe value of the corresponding device parameter.

The sensitivity rate of a particular device parameter on the process parameter fluctuations can be calculated using the following equation:

\[ \frac{\partial V_{th}}{\partial L_{	ext{spec}}} = \left( \frac{\partial \delta V_{th}}{\partial \delta L_{	ext{spec}}} \right) \left( \begin{array}{c} \sigma_{\delta V_{th}}^2 \sigma_{\delta L_{	ext{spec}}}^2 \\ \sigma_{\delta V_{th}}^2 \sigma_{\delta L_{	ext{spec}}}^2 \\ \vdots \end{array} \right) \left( \begin{array}{c} \sigma_{\delta L_{	ext{spec}}}^2 \sigma_{\delta V_{th}}^2 \end{array} \right) + \ldots \left( \begin{array}{c} \sigma_{\delta V_{th}}^2 \sigma_{\delta L_{	ext{spec}}}^2 \end{array} \right) \right) \left( \begin{array}{c} \sigma_{\delta L_{	ext{spec}}}^2 \sigma_{\delta V_{th}}^2 \end{array} \right) \right) \]

All the quantities in (1) are normalized, which is indicated by the symbol \( \delta \), that is \( \delta V_{th} = (V_{th} - V_{th})/V_{th} \). The meaning of the other symbols in (1) is as follows: \( \sigma_{\delta V_{th}}^2 \) is the variance of the normalized threshold voltage; \( \sigma_{\delta L}, \sigma_{\delta V_{th}}, \ldots, \sigma_{\delta L} \) are the standard deviations of normalized process parameters as listed in Table I; the partial derivatives of the threshold voltage with respect to the process parameters are determined at the points corresponding to the specification (recipe) values of the process parameters. Analogous equations can be written for the other three device parameters under consideration: the transconductance, substrate current, and the off current. It should be noted that generally (1) is a series expansion, including all higher power as well as mixed terms, such as \( \sigma_{\delta V_{th}}^2 \sigma_{\delta L_{	ext{spec}}} \). These terms are not explicitly shown in (1) because our subsequent analysis showed that they could be neglected.

This mathematical model provides a basis for the analytical study of the collective influence of the variances of individual process parameters on the variance of a particular device parameter. Equation (1) clearly indicates that it is not enough to know the standard deviations of process parameters, i.e., \( \sigma_{\delta L}, \sigma_{\delta V_{th}}, \ldots \), but it is also necessary to determine the derivatives such as \( \frac{\partial \delta V_{th}}{\partial \delta L_{	ext{spec}}} \). These derivatives represent the sensitivity of a particular device parameter on the process parameter fluctuations. To find the sensitivity factors, a sensitivity analysis has been performed [8] as described in the following subsection.

A. Sensitivity Analysis

This analysis was again based on typical fluctuations found in semiconductor manufacturing, as shown in Table I. It consisted of varying a processing parameter in discrete steps through two standard deviations (2 \( \sigma \)) on either side of the recipe value, and simulating the device characteristics. Again this was done for the four device parameters and for each of the listed processing parameters. The resulting device parameter values were linearly fitted, to determine the normalized values of the slopes, which are the sensitivity rates. As a reference for comparisons, the sensitivity rates of a typical micron size device (1.25 \( \mu \text{m} \)) were also determined, and plotted together with the results of the deep submicron (0.1 \( \mu \text{m} \)) device in the histograms, which are shown in Fig. 3.

We found that the off current sensitivity due to the channel length was exponential, indicating punch through or avalanche problems [8], [9]. It should be noted that the sensitivity rate for the off-current shown in Fig. 3(d) is a linear approximation, which was done to include this sensitivity rate in our comparative study.
The sensitivity rates shown in Fig. 3 express percentual fluctuation of a device parameter for any percent of change in a process parameter. Not surprisingly, these histograms show that device parameters are very sensitive to temperature fluctuations (e.g., annealing temperatures for channel and source/drain implants). This does not mean, however, that the temperature contribution to the device parameter fluctuation will be the largest, or even significant. The histograms in Fig. 3 express only sensitivity rates, which are the partial derivatives of a device parameter versus process parameters function, as presented in (1). From this equation, one can see that the sensitivity rates, or the partial derivatives have to be multiplied by the standard deviations of the process parameters, and then squared to obtain a figure which represents a process parameter’s contribution to a device parameter fluctuation, or variance. This becomes a fluctuation analysis and it is presented in the following subsection.

B. Fluctuation Analysis

As mentioned above the effective fluctuation contribution \( \varphi \) of a process parameter \( p_i \) to each of the device parameter’s fluctuation is given as squared product of the process parameter’s standard deviation and the corresponding sensitivity rate. As an example for the threshold voltage, this expression would be

\[
\varphi = \left( \frac{\partial \delta V_{th}}{\partial \delta p_i} \right)^2 \sigma_{p_i}. \tag{2}
\]

To summarize and visualize the effective fluctuations, we have represented them again as histograms shown in Fig. 4. These histograms show the effective fluctuation contribution of each processing parameter to each of the electrical device parameters.

As can be seen from Fig. 4, the effective fluctuations of the device parameters, caused by the process parameter fluctuations, are larger in the deep submicron device in most of the cases. Undoubtedly the two most influencing process parameters are the gate oxide thickness and the channel length. The fluctuations of the gate oxide thickness showed the largest influence on the threshold voltage and the transconductance being four times more effective in the case of threshold voltage and transconductance in the deep submicron device, than in its micron sized counterpart. It has almost equal influence on the substrate current in both device sizes, being slightly larger for the deep submicron device, and finally more than ten times as strong on the off current in the deep submicron device.

The other process parameter that provides an influence is the channel length. While the effect of the channel length fluctuations on the transconductance is surprisingly small for both device sizes under scrutiny, its effect on threshold voltage, substrate current, and off-current is again much larger in the smaller device. As discussed in the previous section, the channel length fluctuations have a marked effect on the off current. This together with the increased sensitivity to threshold voltage adjustment implant energy and oxide charges indicates once more that the off-current, which is generally viewed as a nonessential device parameter, should be carefully investigated when designing deep submicron devices.

The fluctuations of the threshold voltage adjustment implant, especially the energy, proved to be the third most important cause of device parameter fluctuations. The two most sensitive device parameters were the off-current (with an effective sensitivity up to 500–600 times larger in the deep submicron device than in the micron size device) and the threshold voltage, with more than ten fold increase of effective sensitivity in the smaller device.

Fluctuations in the source/drain extension implant were less important, although not negligible, especially in view of off-current fluctuations. The off-current’s sensitivity to dose fluctuations is reduced to almost negligible for the smaller device, while the sensitivity to energy is about the same in both devices.

V. DISCUSSION

In this study we have determined the sensitivity rate of each processing parameter in an uncorrelated way, independent of any other influence. The reason why we proceeded in this way was not only simplicity, but because wafers are processed in sequential steps, whose manufacturing fluctuations can be assumed independent of each other. In order to verify this
TABLE II

<table>
<thead>
<tr>
<th>Device Parameter</th>
<th>0.1 ( \mu \text{m} ) device</th>
<th>1.5 ( \mu \text{m} ) device</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>normalized std. deviation</td>
<td></td>
</tr>
<tr>
<td>Threshold voltage</td>
<td>LHS 43</td>
<td>RHS 42.6</td>
</tr>
<tr>
<td>Transconductance</td>
<td>27.3</td>
<td>32.2</td>
</tr>
<tr>
<td>Substrate current</td>
<td>95</td>
<td>69</td>
</tr>
<tr>
<td>Off-current</td>
<td>170</td>
<td>239</td>
</tr>
</tbody>
</table>

For the 0.1 \( \mu \text{m} \) device, the normalized standard deviation is 43%, and for the 1.5 \( \mu \text{m} \) device, it is 46%. These values were obtained from a Monte Carlo simulation, which is a technique used to estimate the behavior of complex systems when the number of variables is high.

We have verified the independence for the considered four electrical device parameters and the 12 processing parameters as listed above. Again all values were normalized with respect to their specification and recipe value.

1. We have found that the theorem could be applied with an accuracy better than numerical errors in all the four cases of device parameters. The values obtained for the standard deviations are given in Table II. These values were obtained from the Monte Carlo simulation, left hand side (LHS) of (1) and from the sensitivity analysis, right hand side (RHS) of the same equation.

2. Comparison of the standard deviations (Table II) provides an insight into the influences of different process parameters. If there were correlations between different process parameters, then neglecting the mixed terms (e.g., \( \frac{\delta \Omega_A}{\delta \Omega_B \Omega_C} \)) on RHS of (1) would cause smaller standard deviations compared to their Monte Carlo simulation counterparts (LHS).

3. The difference however is not significant, keeping in mind that only the current fluctuations towards higher values are undesirable (substrate current fluctuations toward smaller values do not produce any undesirable effects). The discrepancy between both figures in the off-current case cannot be attributed to neglected mixed terms, as the RHS standard deviations are larger than the LHS ones. This discrepancy can only be explained as a numerical error. As one might recall, the gate length exhibited an exponential influence on the off current of the deep submicron device within the range of the typical fluctuation. For the verification of the independence theorem, we have approximated its sensitivity linearly, for the sake of uniformity, introducing a significant error. In the Monte Carlo simulation however, the exponential dependence of the off-current on the channel length is inherently used. Further it should be pointed out that the off-current in the micron size device is virtually inexistent, and that even the smallest process parameter fluctuation appears augmented, which explains the relatively large discrepancy of fluctuation figures.

Our results show 1) that the selection of processing parameters chosen for the study are indeed contributing factors to manufacturing fluctuations to the extent as individually determined, 2) that the influences of the processing parameters can be assumed as uncorrelated to each other with reasonable safety, and 3) that only a few process parameters play a major role as affecting through their manufacturing fluctuations the end results of the device parameters.

VI. CONCLUSIONS

In this study, we have identified the main contributors of processing parameter fluctuations during manufacturing to the resulting electrical device parameter fluctuations. The results of our study have revealed that the effects of typical processing fluctuations can be up to 600 times more pronounced in deep submicron devices, than in their micron size counterparts. These huge changes in sensitivity rates indicate that current manufacturing heuristics and efforts for process control need to be revised for future deep submicron technology. The study has also revealed that the effects of processing parameter are largely independent among each other. It is therefore possible to focus efforts to gain better control to those processes which have greater impact in order to reduce their fluctuations.

REFERENCES


Renate Sitte (S'92) received the engineering degree from the Universidad de los Andes, Venezuela, in 1985, and the M.Phil. in 1992 from Griffith University, Australia.

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H. Barry Harrison received the B.E. degree in electrical engineering (honors) and the Diploma of Education from the University of Melbourne, Australia, in 1965 and 1967, respectively. He received a graduate degree in communication engineering in the Netherlands in 1972.

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