Experimental Study of Parasitically Steered, Fixed Beam Microstrip Patch Arrays

Author
Gray, D., Lu, Junwei, Shafai, L.

Published
1997

Conference Title
IEEE AP-S Digest 1997

DOI
https://doi.org/10.1109/APS.1997.631799

Copyright Statement
Copyright 1997 IEEE. Personal use of this material is permitted. However, permission to reprint/republish this material for advertising or promotional purposes or for creating new collective works for resale or redistribution to servers or lists, or to reuse any copyrighted component of this work in other works must be obtained from the IEEE.

Downloaded from
http://hdl.handle.net/10072/19845

Link to published version
http://ieeexplore.ieee.org/xpl/mostRecentIssue.jsp?punumber=4924
Experimental Study Of Parasitically Steered, Fixed Beam Microstrip Patch Arrays

D.P. Gray*, J.W. Lu+ and L. Shafai*
*Dept. Of Electrical & Computer Engineering, University Of Manitoba, Winnipeg, Canada R3T 5V6 [dgray@ee.umanitoba.ca]
+School Of ME, Griffith University, Nathan, Australia 4111

1. INTRODUCTION

The linearly polarised single feed point Yagi microstrip patch array was developed from the concept of the thin wire Yagi-Uda array [1]. It is possible to build either vertically polarised or horizontally polarised arrays. Both feed points of an array can be excited with the correct amplitude and phasing to produce circular polarisation. A Yagi microstrip patch array produces a mainlobe away from the zenith, and has the advantage of being planar for applications such as wireless office.

The aim of this paper is to investigate the influence of various characteristics of the dielectric substrate upon array performance and to access the effects of reduction of the array size upon its behaviour. The collected experimental data will be useful for comparison to numerical simulations in future work. The linear polarised measurements were undertaken in the anechoic chamber at the University of Manitoba.

2. DESIGN PHILOSOPHY

All array dimensions were calculated at fixed proportions to a square patch on the given dielectric substrate, Figure 1. The same proportions were used for all the arrays to allow for comparison of factors other than the array proportions. These proportions differ slightly from those described by Huang [1]. Four arrays were built and tested for the 1.45-1.75GHz frequency range, and six for the 2.9-3.4GHz range. Three dielectric substrates were used, lexan polycarbonate \( \epsilon_r = 2.88 \), TMM3 \( \epsilon_r = 3.27 \) and TMM4 \( \epsilon_r = 4.50 \).

The commercially available software Ensemble 4.02™ was used to determine the optimal feed positions for each of the arrays, by repeated simulations with different feed positions. The loci for the two different feed positions are indicated in Figure 1. Due to the symmetry of the array, there are two possible H-plane feed points. There are also two possible E-plane feed points; one close to the edge of the driven patch closest to the reflector patch (as used in [1]) and the other close to the edge closest to the director patches.

0-7803-4178-3/97/$10.00 © 1997 IEEE

1276
3. EFFECTS OF USING FORWARD FEED POSITION

As noted above, there are two possible E-plane feed positions. Using the position closest to the director patches significantly reduces the cross-polar level, without changing the co-polar pattern, Figure 2. The $S_{11}$ traces from two arrays that were identical in all ways except the positioning of the E-plane feed shows that the H-plane feed $S_{11}$ were very similar as was expected, Figure 3.

4. EFFECTS OF REFLECTOR PATCH REMOVAL

Removal of the reflector patch would allow for a reduction in the overall size of the array. Removing the reflector patch entirely, or replacing it with a printed dipole, caused a decrease in the co-polar backlobe and an increase in the cross-polar level for higher elevations, Figure 4. This shows that the reflector patch may be removed to reduce the array size without significant degradation of performance.

5. EFFECTS OF GROUND PLANE SIZE INCREASE

Some applications of this type of planar array will require mounting above an extended ground plane. It has been found that the ground plane size is critical to the performance of microstrip patch antennas [2]. An addition of a $0.9 \times 0.9m$ ground plane caused the gain at low elevations to increase, Figure 5.

6. EFFECTS OF DIFFERENT SUBSTRATE THICKNESSES

A number of dielectric substrate thicknesses were trialed in order to gain insight into the effects of the thickness upon array performance. Increasing the substrate thickness from 3.10 to 5.90mm caused a decrease in mainlobe gain and increases in the backlobe and cross-polar levels, Figure 6.

7. EFFECTS OF DIFFERENT DIELECTRIC CONSTANTS

For the arrays constructed on TMM3 and TMM4, the electrical thickness of the dielectric substrates was just over 7% of the dielectric wavelength. Increasing the relative permittivity (3.27 to 4.50) caused an increase in mainlobe gain and decreases in backlobe and cross-polar levels, Figure 7.

8. REFERENCES


Figure 1: Geometry of Yagi microstrip patch array with array element dimensions as percentages of square patch size marked.

Figure 2: Radiation patterns from the 2 possible E-plane feed points at 1.55GHz from the 5.90mm lexan substrate arrays.

Figure 3: S_{11} in dB from the 2 possible E-plane feed points from the 5.90mm lexan substrate arrays.
Figure 4: Radiation patterns without the reflector patch from the 5.90mm lexan substrate array with E-plane feed toward the director patches at 1.55GHz.

Figure 5: Radiation patterns for the addition of 0.9 x 0.9m ground plane from the TMM3 array with E-plane feed toward the director patches at 3.06GHz.

Figure 6: Radiation patterns for different thickness lexan substrates from the arrays with E-plane feeds toward the director patches at 3.06GHz.

Figure 7: Radiation patterns for different relative permitivity substrates having the same electrical thickness from the arrays with E-plane feeds toward the reflector patch at 3.1GHz.