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To cite this article before publication: Junhao Ni et al 2021 Nanotechnology in press https://doi.org/10.1088/1361-6528/ac2fe1

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Status and Prospects of Ohmic Contacts on Two Dimensional Semiconductors

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Abstract

In recent years, two-dimensional materials have received more and more attention in the development of semiconductor devices, and their practical applications in optoelectronic devices have also developed rapidly. However, there are still some factors that limit the performance of two-dimensional semiconductor material devices, and one of the most important is Ohmic contact. Here, we elaborate on a variety of approaches to achieve Ohmic contacts on two dimensional materials and reveal their physical mechanisms. For the work function mismatch problem, we summarize the comparison of barrier heights between different metals and 2D semiconductors. We also examine different methods to solve the problem of Fermi level pinning. For the novel 2D metal-semiconductor contact methods, we analyse their effects on reducing contact resistance from two different perspectives: homojunction and heterojunction. Finally, the challenges of 2D semiconductors in achieving Ohmic contacts are outlined.

Keywords: two-dimensional materials; Ohmic contact; work function; Schottky barrier; Fermi level pinning; transferring electrode; 2D metal-semiconductor contact

1. Introduction

In the past half century, the maturity of complementary metal oxide semiconductor (CMOS) field effect transistor (FET) devices based on silicon (Si) has enabled modern information technology to develop by leaps and bounds [1, 2]. According to Moore's Law, the integration density of FETs on a chip can be doubled and the feature size of FETs can be shrunk greatly in every two years [3]. When the channel length is close to a few nanometers, problems such as short channel effect and high heat dissipation will emerge, indicating that traditional silicon-based devices are approaching their limits. Therefore, it is urgent to explore new alternative materials [4-6]. In this context, 2D semiconductor materials such as graphene [7], transition metal dichalcogenides (TMDC) [8-12], Black Phosphorous (BP) [13, 14], etc. have received more and more attention in recent years. In particular, Ajayan et al. has made a comprehensive summary on the characteristics, preparation and application of different 2D semiconductor materials [15]. The 2D materials are so atomically thin that the channel length of FETs based on such materials can be further reduced without short channel effect. Moreover, due to their unsuspended surface, the devices based on such materials can greatly eliminate carrier scattering, representing an obvious advantage over traditional materials like Si [16-20]. These excellent features of 2D materials have made them promising for a variety of electronic and optoelectronic devices including field effect transistors [21], photodetectors [22], logic devices [23], light-emitting diodes [24] etc.
The contacts between metallic electrodes and semiconductor materials are crucial components of electronic and optoelectronic devices [25], especially for 2D materials that are always accompanied by 3D metallic electrodes. In fact, contacts are the communication bridge between 2D materials and the three-dimensional world [26]. Such contacts are either Ohmic contacts or Schottky junctions. The latter may result in a huge contact resistance and thus hinder the transport of carriers [27, 28], degrade the transconductance [29], switching current ratio and other properties. Therefore, for most devices, Ohmic contact is a guarantee of high performance [26, 30-32]. To this end, researchers have explored many new methods in recent years to achieve Ohmic contacts or reduce 2D contact resistance, such as one-dimensional contact [33-36]; phase engineering [37-39]; electrode transfer [40] and so on. Precise engineering of ohmic contacts in two-dimensional materials is rapidly gaining momentum as summarized in a recent review [41]. This article complements the analysis of the recent advances in this area by examining the effective approaches to fabricate Ohmic contacts on 2D materials as well as the relevant physical mechanisms.

In this review, we will elaborate on the latest research progress of different methods in achieving Ohmic contacts or reducing contact resistances for 2D materials devices. They can be mainly classified into three ones: using metal electrodes with matched work function, solving the problem of Fermi pinning level and using 2D metal-semiconductor contacts. We first examine the work function matching between the most widely studied 2D materials like graphene, TMDCs and BP and different metals, and pinpoint the most suitable metals for these 2D materials in achieving Ohmic contacts. We then study the problem of Fermi level pinning and reveal the physical mechanism. Through different contact engineering, the Fermi level pinning effect at the interface can be weakened and even almost completely suppressed. Finally, in the third part, we shift our sight from 3D to 2D, and focus on effective methods for 2D metal materials to reduce contact resistance from the perspective of homojunction and heterojunction. The main motivation of this review is to establish the associations between different contact methods and the properties of 2D semiconductors to facilitate our understandings toward how to realize Ohmic contacts on 2D semiconductors and improve the performance of their devices.

2. Using metal electrodes with matched work function

In theory, the contact resistance mainly depends on the height of the Schottky barrier. When the metal electrode and the semiconductor interface are in contact, due to the difference between the metal work function and the semiconductor affinity (ionization potential), the energy band will be bent and the Schottky barrier will be generated. Under ideal circumstances, according to Schottky-Mott's law [42, 43]:

$$\Phi_{B,n} = \Phi_M - \chi_{SC}$$

(1)

$$\Phi_{B,p} = I_{SC} - \phi_M$$

(2)

where $\Phi_{B,n}$ and $\Phi_{B,p}$ are the Schottky barrier heights for electron and hole injection respectively. $\Phi_M$ is the metal work function, $\chi_{SC}$ and $I_{SC}$ are the electron affinity and ionization potentials respectively. In theory, we can minimize the height of the Schottky barrier by selecting a suitable metal electrode to reduce the contact resistance. The minimum of the Schottky barrier is zero corresponding to Ohmic contact. Therefore, we will systematically study the work function matching between different metals and the most widely studied 2D materials like graphene, TMDC (mainly MoS$_2$) and BP in achieving Ohmic contacts in this section.

2.1 Graphene

Unlike other 2D materials, graphene has a zero-band gap, so it doesn’t form a depletion layer and traditional Schottky barrier when it contacts with metal. The contact between metals with different work functions and graphene will cause graphene to be electrically doped, causing the Fermi level of graphene to move away from the Dirac point. Although graphene has good conductivity, the small density of states near the Dirac point limits the carrier transfer efficiency between metal and graphene, resulting in high contact resistance in the contact area [30]. By using the transmission line model measurement method, Osman et al. measured the contact resistance of Au and graphene to be 630 $\Omega$·$\mu$m, Ag-630 $\Omega$·$\mu$m, Cu-8800 $\Omega$·$\mu$m, Pd-570 $\Omega$·$\mu$m [44] and Ni-2200 $\Omega$·$\mu$m [45]. It is obvious that the contact resistance of Pd and Au is the smallest and this is why we often use Au as the metal electrodes in daily experiments. In addition to a single metal electrode, a combination of multiple metals is generally employed in daily experiments. For this reason, the contact resistance of a combination of multiple metals in contact with graphene is also listed. Au is the most widely used choice among the combined metals. When the previous single metal and Au are combined to serve as the contact, the contact resistance drops significantly. The contact resistance of Cu/Au drops to 92 $\Omega$·$\mu$m [46], which is reduced by two orders of magnitude.
Pd/Au-122 Ω·μm [46]; Ni/Au-400 Ω·μm [45]; Ti/Au- 23Ω·μm [46]. It is clear that Ti/Au and Cu/Au mixed contacts are the most appropriate for graphene in reducing the contact resistance.

2.2 TMDCs and BP

As a typical n-type representative semiconductor of TMDCs, MoS₂ has received extensive attention due to its excellent performance. Here, we mainly study the Schottky barrier height when MoS₂ is in contact with metals with different work functions, and summarize the metal-semiconductor contacts closest to Ohmic contact. The left side of Figure 1 (a) is the energy band of MoS₂ and the work function arrangement of commonly used metals, and the right side is the n-type Schottky barrier height where different metals contact with MoS₂. Among them, the red part is the electronic Schottky barrier height obtained through theoretical calculation [9], and the blue part is the Schottky barrier height extracted in the actual experiment [11, 47, 48]. It can be found that the Fermi levels of Sc and Ti are closer to the conduction band of MoS₂, and the Schottky barrier heights between Sc and MoS₂ as well as between Ti and MoS₂ are the smallest. Comparing theoretically calculated barrier heights with actual ones for all the metals, one can find a large difference between them except the metals including Au and Pt. This means that Au and Pt metal electrodes are very hardly affected by the surface states. In addition to Sc and Ti, Kang et al. have also studied metal Mo as an electrode in contact with MoS₂. Because Mo is the constituent metal of the semiconductor material, strong atomic orbital overlap occurs at the contact interface, thus giving rise to a smaller contact resistance than Ti. As show in Figure 1(b), the SBH of Mo-MoS₂ system is much less than Ti-MoS₂ system [49, 50]. In addition, Wang et al. have achieved van der Waals contacts (as shown in Figure 1(c)) to reduce contact resistance by evaporating 10 nm thick In between 100 nm thick Au and a single layer of MoS₂ [51]. The experimentally extracted Schottky barrier is about 110 meV, and the contact resistance on the single-layer MoS₂ is measured to be about 3 kΩμm, and that on the multilayer MoS₂ is 0.8 kΩμm, which is one of the smallest values so far. This resulted in a huge increase in mobility (Figure 1(c)) [51]. In brief, in addition to Sc and Ti with low work function, the special metals Mo and In can also achieve almost Ohmic contact for MoS₂. Similarly, for another n-type TMDCs like WS₂, the most appropriate metals to achieve Ohmic contact are Sc, Ti, W and In [49]. However, for typical p-type TMDCs like WSe₂, the metals with high work function are preferred instead to achieve Ohmic contact. As shown in Figure 1(d), the Schottky barrier heights of holes between WSe₂ and different metals are 1.27, 1.25, 1.13, 0.9 and 0.35eV, corresponding to Ti, W, In, Au, and Pd. Therefore, the most appropriate metals to achieve Ohmic contact for WSe₂ is Pd.

BP shows great potential in 2D electronic and optoelectronic devices because of its high mobility and direct band gap. Few-layer BP flakes with 3-15 layers are often used in practice, which possess a bandgap ranging from 0.3 to 0.6 eV. The band gap can be calculated theoretically: \( E_{gap} = 0.39eV + 1.62/n^{1/4} \) (n=number of layers). As the number of BP layers increases, the band gap decreases. In order to study the influence of different metals and BP contact, Wang et al. listed the Schottky barrier heights extracted from five different metals Ti, Ni, Ag, Sc, Er in contact with BP under the aluminia package [52] (Figure1(e)). It can be seen from the figure that Ni has the lowest hole barrier, which is more conducive to hole injection, so as to achieve high-quality p-type contact, which is also proved by experimental results. Zhi-peng et al. reported that when using Ni in contact with 12nm thick BP, the extracted Schottky barrier is 92meV; when using 15nm BP in contact with Ni, Ni,P is formed after annealing at 300°C, making the contact potential barrier reduced to 12 meV [53]. Compared with low work function metals Co and Ti as contact metals (extracted Schottky barriers are 206meV [54] and 325meV [55], respectively), Ni shows obvious advantages. In addition to Ni, high work function metal Pd is also a common metal. Yuqiang Ma et al. synthesized Pd-H alloy by reacting with H₂ to increase the work function of the metal, thereby reducing the barrier height and making the contact resistance from 7.1Ω·mm is reduced to 1.05Ω·mm [56]. As a supplement to BP performance, bipolar transport can be obtained by using Al (φₓ=4.2eV) with BP contact, which provides more possibilities for the application of BP in CMOS [57, 58].

3. Solving pinning effect

Ideally, the height of the Schottky barrier between a metal and a semiconductor is determined by the metal work function, but in practice, the \( \Phi_B \) in formula (1) is usually insensitive to \( \Phi_m \). This is mainly due to the fact that the Fermi level of the system is typically pinned to a nearly fixed position in the semiconductor bandgap, varying little with respect to different metals used. The Schottky-Mott law should be rewritten as following when considering the Fermi pinning effect [59]:

\[
\Phi_B = S(\Phi_M - \Phi_{CNL}) + (\Phi_{CNL} - \chi)
\]  \( \text{(3)} \)
where $S$ is the Schottky pinning coefficient ($S = \frac{\partial \phi_T}{\partial \phi_M}$), $\chi$ is the electron affinity of the semiconductor, and $\phi_{\text{CNL}}$ is the charge intermediate performance level under a relative vacuum level. Generally, the Fermi level will be pinned near $\phi_{\text{CNL}}$. It is given by the following formula:

$$\phi_{\text{CNL}} = \frac{\chi + b}{1 - S}$$

where $b$ is the $y$-intercept of the $\phi_T$ versus $\phi_M$ graph, $S$ depends on the interface state density per unit area $N$, and the length $\delta$ extending into the semiconductor as:

$$S = \frac{1}{1 + \frac{\varepsilon_I N_S^2}{\varepsilon_{CS}}}$$

where $\varepsilon_{\\text{CS}}$ is the absolute permittivity of the interface. When $S$ is equal to 0, strong Fermi level pinning effect dominates so that SBH has nothing to do with the work function of the metal; when $S$ is close to 1, the Barding limit converges to the Schottky limit.

The origin of Fermi level pinning may be mainly due to the existence of interface dipoles and metal-induced interstitial states (MIGS) [60]. The interface dipoles will change the metal work function, and the metal-induced interstitial states will weaken the layer Molybdenum-sulfur bond through the interaction between the metal and the chalcogenide element, thereby pinning the Fermi level [60]. In addition, the existence of defects is considered to be one of the main reasons for Fermi level pinning [61, 62]. The SBH difference versus the lateral and experimental values as illustrated in Section 1.2 may be caused by the defect state, which is attributed to atomic vacancies and process-induced defects [63]. Studies on the pinning factor of the defect area and the original surface show that in most cases, the pinning factor of the defect area is observed to be about 30-40% smaller than that of the original surface, which indicates that the FLP is stronger at the defect location [64]. Guo et al.’s study of Schottky barrier heights for a variety of metals showed that any defects formed at the interface will cause additional pinning and reduce the $S$ value [65]. Therefore, improving the quality of the interface is essential for achieving high-performance Ohmic contacts.

3.1 Reducing interface impact

One of the main methods to alleviate the Fermi level pinning effect is to insert an ultra-thin oxide layer between the metal and the semiconductor to realize the MIS structure. The insertion of the oxide layer can isolate the direct contact between the metal and the 2D semiconductor, and thus suppress the Fermi level pinning effect by reducing the metal-induced interstitial state. Also, the insertion of the oxide layer can ensure the cleanness of the interface. Various metal oxides have been used as insulating layers to reduce the pinning effect, such as MoO$_3$, MgO, Ta$_2$O$_5$, TiO$_2$, and so on [66-69]. As early as 2013, Chen et al. found that by inserting MgO between Co and MoS$_2$, the height of the Schottky barrier can be reduced by 84% [66]. In addition to MgO, TiO$_2$ is also a commonly used oxide. In 2014, Dankert et al. also used Co as a contact and changed the interface state by inserting a TiO$_2$ insulating layer between Co and MoS$_2$. As a result, the height of the extracted Schottky barrier was reduced from 121 meV to 27 meV, and the on-state current of the transistor increased by two orders of magnitude, as shown in Figures 2(a) and 2(b) [67].

Recently, Kim has further studied the reason why the oxide layer alleviates the Fermi level pinning effect by inserting an ultra-thin TiO$_2$ between various metals and MoS$_2$. As shown in Figures 2(c) and 2(d), the pinning coefficient $S$ is increased from 0.02 to 0.24 after inserting TiO$_2$, showing a stronger work function dependence. The Fermi level pinning in the intermediate state is moved more toward their ideal position through the reduced interface state of the oxide layer, thereby weakening the pinning effect [68]. Lee et al. reduced the barrier height from 95 meV to 29 meV by inserting Ta$_2$O$_5$ between MoS$_2$ and metal contacts [69]. As shown in Figure 2(e), the authors believed that the MIS structure can alleviate the Fermi level pinning mainly due to two points: (1) The metal will make the semiconductor Fermi level move to the charge middle performance level, and the oxide layer can weaken this effect; (2) Interfacial dipoles will form on the interface of oxide and semiconductor, and the opposite electric field of the interfacial dipoles will also restrict the Fermi energy level to move to the charge middle performance level. At the same time, Lee et al. also found that the thickness of the oxide layer also had an effect on the barrier height. The barrier height will increase as the oxide layer thickness increases [69].

In addition to the oxide layer, inserting h-BN to form the MIS structure can also effectively suppress the Fermi level pinning effect [70, 71] with similar physical mechanisms. BN can help form a high-quality interface with 2D semiconductors to prevent the formation of defects and traps. Wang et al. reduced the barrier height from 158 meV to 31 meV and the contact resistance from 5.1 kΩμm to 1.8 kΩμm by inserting h-BN between MoS$_2$ and Ni/Au [70]. In addition to intercalation, it is also a feasible method to reduce the Fermi level pinning effect by treating...
the metal surface. Min et al. weakened the Fermi level pinning effect by atomically passivating gold, reducing the barrier height to 0.12eV, almost realizing Ohmic contact [72]. In addition, ultra-high vacuum deposition of metal electrodes can make the metal/semiconductor interface extremely clean and thus greatly reduce the contact resistance. For example, the contact resistance of MoS\(_2\)/Au contact with the metal electrode deposited under ultra-high vacuum conditions (10\(^{-6}\) Torr) is three times lower than that under normal conditions (10\(^{-6}\) Torr) [73]. Recently, Matkovic, et al. successfully adjusted the work function of the metal electrode by using a self-assembled monolayers(SAMs) containing pyrimidine, so that the contact resistance between MoS\(_2\) monolayer and metal electrode was reduced by two orders of magnitude. This work brings a new but well-understood approach in organic electronics to realize ohmic contact in the field of 2D materials [74].

3.2 Transferring electrode

The above-mentioned multiple methods can partially reduce the Fermi level pinning effect, while transferring electrode as proposed and demonstrated by Liu et al. can almost completely eliminate the Fermi level pinning effect [40]. By pre-fabricating metal electrodes and transferring them onto the two-dimensional semiconductor surface via physical lamination, related chemical barriers and defect-induced gap states are completely eliminated. As shown in Figures 3(a) and 3(b), compared to the metal/semiconductor interface prepared by traditional deposition method, that prepared by transferring electrode method has an atomically sharp and clean interface, without defects, strain, metal diffusion, etc. Physical contact without direct chemical bonding can greatly inhibit the generation of interstitial states induced by interface dipoles and metals, thereby eliminating Fermi level pinning. Figure 3(c) shows the transfer characteristic curves of MoS\(_2\) FETs with electron beam deposited metal electrodes. It can be seen that regardless of the different work function of the contact metals, all curves shown-type behaviour. This indicates that there is a strong Fermi level pinning effect near the MoS\(_2\) conduction band. On the contrary, for the FETs with transferring metal electrodes, as shown in Figure 3(e), the transfer curves can exhibit different polarities with the change of the work function of different metals. For low work function metal like Ag, a good n-type transfer curve is observed; as the work function of the metal increases, it shows a transition from n-type to bipolar and then to p-type. By using the extracted Schottky barrier height, the Fermi level pinning coefficient is fitted in Figure 3(d). For the FET devices with deposited metal, the extracted S parameter is 0.09, which is consistent with the previous results. For the devices with transferring electrode, the fitted S parameter is 0.96, which is approaching the Schottky-Mott limit, indicating that the height of the Schottky barrier depends dominantly on the metal work function.

4. 2D-semiconductor-metal contact

Using 2D metal materials instead of traditional metals to contact 2D semiconductors is also a good strategy to eliminate the Fermi level pinning effect. Homogeneous structures or heterostructures formed by 2D metals and 2D semiconductors can strongly inhibit the formation of metal-induced interstitial states in semiconductors, thereby reducing the Fermi level pinning effect. In this section, we thus elaborate on effective methods for 2D metal materials to reduce contact resistance from the perspective of homojunction and heterojunction.

4.1 Homojunction

For TMDCs, in addition to the 2H phase semiconductor state, they also have 1T and 1T' metal states. As shown in Figure 4(a), both 2H and 1T (1T') phases possess the same lattice constant so that their in-plane connections are perfectly aligned without any interface defects [75, 76]. For example, as shown in Figure 4(b), it is effective to grow lateral homojunction consisting of 2H MoTe\(_2\) and 1T MoTe\(_2\) by chemical vapor deposition via adjusting growth temperature since the energy difference between the 1T phase and the 2H phase is very small (35 meV per unit cell) [77, 78]. Compared with the traditional contact consisting of 2H semiconducting phase MoTe\(_2\) and metal electrode where a Schottky contact is always formed, a novel contact consisting of 2H/1T' homojunction and metal electrode is fabricated where a stable covalent bond is formed between the 1T' phase and the 2H phase. In this case, the 1T' metallic phase serves as the transition region between 2H semiconducting phase and metal electrode. As a result, the contact resistance is reduced significantly and the conductance of the 2H semiconducting channel is increased by two orders of magnitude. As shown in Figure 4(c), the height of the Schottky barrier is reduced greatly from 150 meV for the traditional contact consisting of 2H MoTe\(_2\) and metal electrode to 22 meV for the novel contact consisting of 2H/1T' homojunction and metal electrode. Such Chemical Vapor Deposition (CVD) method can also be applicable to the fabrication of WSe\(_2\) 2H/1T homojunction for reducing the Fermi level pinning effect [79, 80].
In addition, phase engineering of existing 2H-phase TMDCs to transform them into metallic phases is also a commonly used method [37, 38, 81]. As early as 2014, kappers et al. [37] transformed the 2H phase of MoS$_2$ into 1T phase through a local induction method, as shown in Figure 4(d). The contact resistance measured under zero grid bias is only 200 $\Omega \mu$m, which is the lowest value ever for MoS$_2$ [37]. Cho et al. also realized the transformation of MoTe$_2$ from 2H phase to 1T' phase by laser burning method and systematically studied their Schottky barriers. The Schottky barrier height in the 1T' phase is only 10 meV and the field-effect mobility is increased by 50 times [82], indicating that Ohmic contact is nearly realized.

Interestingly, for some specific 2D materials such as PtSe$_2$, the phase state depends on the thickness of the material. The thick layer behaves as a metal state, while the few layers becomes a semiconductor state. In such case, homogeneous junction can be fabricated by superimposing two PtSe$_2$ layers with different thicknesses. Indeed, Das et al. achieved a low-resistance Ohmic contact by transferring multiple layers of PtSe$_2$ onto three layers of PtSe$_2$ (Figure 4(e)) [83]. The barrier height was reduced from 140 meV for the traditional contact consisting of 3L PtSe$_2$ and metal electrode to 35 meV for the novel contact consisting of ML PtSe$_2$/3L PtSe$_2$ and metal electrode [83].

4.2 Heterojunction

In addition to fabricating 2D metal/semiconductor homojunctions made of different phases of the same material to reduce the contact resistance, fabricating 2D metal/semiconductor heterojunctions made of different 2D materials is also an alternative choice. Graphene, as a metallic 2D material with adjustable Fermi level, is usually used as the intermediate metal layer [84, 85]. As shown in Figure 5(a), Chee et al. fabricated the MoS$_2$ transistor by using graphene as the intermediate metal layer between MoS$_2$ and Ag electrode [84]. The output characteristics of the controlled MoS$_2$ transistor without using graphene and the one with 2D metal/semiconductor heterojunctions under different gate bias are presented in Figure 5(b), clearly demonstrating that the contact resistance of the latter device is reduced to 1.4% of the controlled one [84]. Apart from graphene, there are many metallic 2D materials such as VS$_2$, VSe$_2$, NbS$_2$, the 1T or 1T' phase TMDs mentioned in Section 4.1. Ji et al. transferred the thin layer VS$_2$ (<10 nm) produced by the CVD method onto MoS$_2$, and the source-drain current under the same bias voltage was increased by 4 times in comparison with that of the MoS$_2$ device with the traditional Ni/Au electrode. Compared to 1T-MoS$_2$ or 1T'-MoTe$_2$, 1T-VS$_2$ exhibits stronger thermal stability, and its performance does not decline even after a few months [86].

Compared to the above transferring method, 2D semiconductor/metal heterojunctions grown directly by chemical vapor deposition have higher interface quality. Recently, Leong et al. have directly grown the lateral MoS$_2$/VS$_2$ heterostructure by CVD method. As shown in Figure 5(d), the lateral heterostructure shows good contact characteristics. The Schottky barrier height of the lateral heterostructure is only 30 meV, while the Schottky barrier height of the MoS$_2$/Ni contact is as high as 163 meV, and the field effect mobility is increased by 6 times [87]. Such clean vdW interface can greatly suppress the interface dipole and metal-induced interstitial states. Further, by selectively forming nucleation points on semiconductor-TMDs, the growth of metallic-TMDs can be precisely controlled to form the van der Waals heterostructure arrays, as shown in Figure 5(e). In particular, WSe$_2$ is used as the channel material, and the adjacent VSe$_2$ layers are used as the source and drain electrodes respectively, so that 2D semiconductor/metal heterojunctions server as the contact. The output curve shows a satisfactory Ohmic contact, as shown in Figure 4(f). The calculated mobility is as high as 137 cm$^2$/V·s$^{-1}$, which is one of the highest values of WSe$_2$ reported so far [88].

Conclusion and Outlook

In conclusion, realizing Ohmic contact is essential to achieve good device performance. Work function mismatch and Fermi level pinning are the main factors that hinder the formation of Ohmic contact. By comparing the barrier heights between different metals and 2D materials, we summarized the most suitable metals for different 2D materials including graphene, commonly used TMDs and BP. However, due to the existence of Fermi level pinning, the influence of the metal work function is weakened. We then mainly provided the mathematical representation of Fermi level pinning and presented two main methods namely intercalating an ultrathin insulating layer and transferring electrodes to reduce the influence of Fermi level pinning. Finally, we elaborated on 2D metal/semiconductor contacts including homojunction and heterojunction to reduce the contact resistance.

In addition, the use of the emerging two-dimensional materials such as silicene, germanene and borobenzene as electrodes to realize ohmic contact has also been reported recently. Yang et al. constructed Ge and MoSSe contacts on different surfaces by taking advantage of the lattice matching between Ge and MoSSe, and thus transformed the Schottky contact into ohmic contact via chaning the tensile strain [89]. Also, Zhao et al.
systematically studied the interface properties of β12 phase borophene and commonly used two-dimensional materials, and found that it can form ohmic contacts with most group V-enes (such as BP) [90].

One important area for future research and development of nanofabrication techniques is the formation of edge contacts. Using plasma etching in combination with photoresist mask to expose the complete boundary of two-dimensional materials such as graphene, and then depositing electrodes to form edge contacts. Such edge contacts offer the advantages of strong orbital overlap and no current crowding [33,34]. However, due to the complexity of the process, it is currently challenging to achieve pure edge contact with standard lithography technology, because the photoresist will inevitably form a trapezoidal slope during the process of revealing and hiding, resulting in the formation of short and small top contact between electrodes and materials. Therefore, it is necessary to overcome this limitation and use more accurate methods to make edge ohmic contacts (for example, using h-BN packaging [91], etc.), which requires more exploration in the future.

Although great strides have been made on how to reduce the contact resistance of 2D devices, there are still some challenges that need to be overcome. First of all, the Fermi level pinning effect is widespread in the contact between metal and 2D semiconductor, but its root cause has not been clearly explained so far. For specific materials, the role of metal interstitial states and interface dipoles can be explained as energy level pinning. But there is still no specific theoretical explanation. Secondly, for traditional semiconductor materials, Ohmic contact is mainly and effectively achieved through high doping. However, the reliable and controllable doping of 2D materials is limited, which needs further investigation. Finally, 2D metal materials are an effective means to achieve Ohmic contact. However, compared with 2D semiconductor materials, the number of 2D metal materials is very limited. Therefore, new methods for the preparation of 2D metal materials need to be further explored.

Acknowledgements

This work is partially supported by the National Nature Science Foundation under Grants 62074070 and 11704159, the Natural Science Foundation of Jiangsu Province, China under Grant BK20170167, the Fundamental Research Funds for the Central Universities of China under Grants JSRP221015, the 111 Project under Grant B118, as well as by the Australian Research Council (ARC).

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Figure 1: TMDCs and BP are in contact with metals with different work functions. (a) The energy band of MoS$_2$ and the work function arrangement of commonly used metals (left side), and the n-type Schottky barrier height where different metals contact with MoS$_2$ (right side). The red part is the electronic Schottky barrier height obtained through theoretical calculation [9], and the blue part is the Schottky barrier height extracted in the actual experiment [11, 47, 48]. (b) Band structure of Mo-MoS$_2$ and Ti-MoS$_2$ [50]; (c) Van der Waals contact between In and MoS$_2$ [51]; (d) Barrier heights of five different metals in contact with WSe$_2$ [49]; (e) The Schottky barrier heights of five commonly used metals in contact with BP (N-SBH, p-SBH) [52].
Figure 2: Oxide intercalation weakens the Fermi level pinning effect. (a) MoS\textsubscript{2} output curve before and after oxide intercalation under zero gate voltage. (b) The height of the Schottky barrier extracted from Figure 2(a)[67]. (c) Schematic diagram of the structure of MS S/D contact and MISS/D contact. (d) The height of the Schottky barrier extracted before and after TiO\textsubscript{2} intercalation, and the fitted pinning coefficient S[68]. (e) Schematic diagram of the Fermi level of MoS\textsubscript{2} pinning, and two explanations for reducing the Fermi pinning effect [69]. (f) Inserting h-BN to form the MIS structure reducing the barrier height from 158 meV to 31 meV (the illustration is the device structure diagram) [70].
Figure 3: (a)-(b) Cross-sectional schematics and TEM images of the transferred Au electrode on top of MoS$_2$ and conventional electron-beam-deposited Au electrode on top of MoS$_2$. The transferred metal-semiconductor surface is very clean and atomically sharp, while electron beam evaporation creates considerable damage to the MoS$_2$ surface, producing a glassy layer with apparent defects, interface diffusion, chemical bonding and atomic disorder. (c) Transfer characteristic curves of MoS$_2$ transistors with deposited and transferred metal electrodes. (d) Experimentally determined Schottky barrier height for different transferred metals and evaporated metals [40].
Figure 4: 2D metal and 2D semiconductor homojunction. (a) In-plane atomic connection diagrams for 1T-MX₂ and 2H-MX₂ as well as 1T'-MX₂ and 2H-MX₂ homojunctions[76]. (b) Schematic and SEM image of the growth of lateral homojunction consisting of 2H MoTe₂ and 1T MoTe₂ by chemical vapor deposition via adjusting growth temperature[78]. (c) The extracted Schottky barrier heights for the novel contact consisting of 2H/1T' homojunction and metal electrode as well as the traditional contact consisting of 2H MoTe₂ and metal electrode [78]. (d) MoS₂ is transformed from 2H phase to 1T phase through phase engineering, and the contact resistance of the corresponding transistor is calculated under zero gate bias[37]. (e) A false-color top-view SEM image of the representative few layer PtSe₂ FETs and the corresponding extracted Schottky barrier height[83].
Figure 5: 2D metal and 2D semiconductor heterojunction. (a) Schematic diagram of graphene being transferred to MoS$_2$ as an electrode. (b) Output characteristics of MoS$_2$ FETs with Ag and graphene/Ag contacts under different gate bias voltage [84]. (c) Schematics showing monolayer MoS$_2$ field-effect transistors (FETs) with the lateral VS$_2$ contacts (upper) and vertical Ni contacts (lower). (d) Schottky barrier heights ($\Phi_B$) of VS$_2$ and Ni-contacted MoS$_2$ devices, as a function of VG. Dashed arrows indicate flat-band $\Phi_B$ [87]. (e) Typical optical microscopy images of rectangular periodic arrangements of VSe$_2$/WSe$_2$ vdWH arrays. (f) Comparison of output curves of van der Waals contacts and WSe$_2$ transistors with direct deposition metal contacts [88].